

CONTINUATION-IN-PART PATENT APPLICATION TRANSMITTAL
(37 C.F.R. § 1.53(b))

22141 U.S. PTO
10/646415
08/22/03

Attorney Docket No.: BDG018

Inventors: Chia-Chung Wang of Hsinchu, Taiwan
Charles W.C. Lin of Singapore

Title: METHOD OF MAKING A SEMICONDUCTOR CHIP ASSEMBLY WITH A
CONDUCTIVE TRACE AND A SUBSTRATE

Prior Group Art Unit: 2818
Prior Examiner: P. Dang

Box Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This patent application is a CONTINUATION-IN-PART application of pending prior U.S. Application Serial No. 09/962,754, filed September 24, 2001, which is a continuation-in-part of U.S. Application Serial No. 09/878,626, filed June 11, 2001, which is a continuation-in-part application of U.S. Application Serial No. 09/687,619, filed October 13, 2000.

Enclosed for filing with the above-identified utility patent application, please find the following:

1. [X] Application, consisting of
 Specification (95 pages)
 Claims (90 pages)
 Abstract (1 page)
 Drawings (180 sheets) (formal)
2. [X] Declaration
3. [X] Assignment Papers (*Cover Sheet & Assignment*)
4. [X] Power of Attorney
5. [X] Statement under 37 C.F.R. § 3.73(b) Establishing Right of Assignee to Take Action
6. [X] Information Disclosure Statement (IDS/PTO-1449)
7. [X] Statement Claiming Small Entity Status by Small Business Concern
8. [X] Request for Nonpublication of Application
9. [X] Return Postcard (MPEP 503) (*specifically itemized*)

FEE CALCULATION:

	(COL. 1) NO. FILED			(COL. 2) NO. EXTRA	SMALL ENTITY	
					RATE	FEE
BASIC FEE:						\$375
TOTAL CLAIMS:	500	-	20	480	X \$9 =	\$4320
INDEP. CLAIMS:	25	-	3	22	X \$42 =	\$924
MULTIPLE DEPENDENT CLAIMS: 0					+\$140 =	\$0
TOTAL FILING FEE						\$5619

OTHER INFORMATION:

1. Charge the \$5619 to Deposit Account No. 502178/BDG018 and charge any underpayment or credit any overpayment to this Account.
2. The application is assigned to Bridge Semiconductor Corporation.
3. Correspondence Address:

David M. Sigmond
2440 Andrew Drive
Superior, Colorado 80027

Telephone: (303) 554-8371
Facsimile: (303) 554-8667

EXPRESS MAIL MAILING LABEL NUMBER: EU267708750US
DATE OF DEPOSIT: AUGUST 22, 2003

ADDRESSEE: BOX PATENT APPLICATION
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Respectfully submitted,



David M. Sigmond
Attorney for Applicant
Reg. No. 34,013
(303) 554-8371
(303) 554-8667 (fax)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

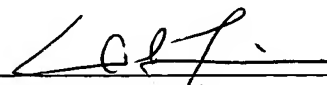
Applicant: Chia-Chung Wang et al.
Assignee: Bridge Semiconductor Corporation
Title: METHOD OF MAKING A SEMICONDUCTOR CHIP
ASSEMBLY WITH A CONDUCTIVE TRACE AND A
SUBSTRATE
Serial No.: Unknown Filed: Herewith
Examiner: Unknown Group Art Unit: Unknown
Atty. Docket No.: BDG018

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**STATEMENT UNDER 37 C.F.R. § 3.73(B)
ESTABLISHING RIGHT OF ASSIGNEE TO TAKE ACTION**

Bridge Semiconductor Corporation is the assignee of the entire interest in the above-identified application by virtue of an Assignment from the inventor(s) of the above-identified application. The Assignment is submitted herewith for recordal in the Patent and Trademark Office. A copy of the Assignment is attached.

The assignee seeks to take action in the Patent and Trademark Office in this matter. The undersigned is empowered to sign this Statement on behalf of the assignee.

By: 
Charles W.C. Lin
Chief Executive Officer
Bridge Semiconductor Corporation

Date: Aug 19, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chia-Chung Wang et al.
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REQUEST FOR NONPUBLICATION OF APPLICATION

Applicant requests that the above-identified application not be published under 35 U.S.C. § 122(b).

Applicant certifies that the invention disclosed in this application has not been and will not be the subject of an application filed in another country, or under a multilateral international agreement, that requires publication at eighteen months after filing.

This request is being submitted with this application on filing.

Respectfully submitted,



David M. Sigmond
Attorney for Applicant
Reg. No. 34,013
(303) 554-8371
(303) 554-8667 (fax)